### Poster Session Schedule

**Poster Session A**  
**Time & Date:** 17:45-18:45, Wednesday, Oct. 26  
**Location:** Hall

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<tr>
<th>Paper ID</th>
<th>Title</th>
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| PA01     | 0006  | A Thermal Model for the Top Layer of 3D Integrated Circuits Considering Through Silicon Vias  
Fengjuan Wang*, Zhangming Zhu, Yintang Yang, Ning Wang  
*Email: cicy4@hotmail.com  
Xidian University, China |
| PA02     | 0026  | Novel High Uniformity Readout Circuit Allowing Microbolometer to Operate with Low Noise  
Jian Lv*, Yun Zhou, Baobin Liao, Yadong Jiang  
*Email: lvjian@uestc.edu.cn  
University of Electronic Science and Technology of China, China |
| PA03     | 0031  | Monolithic Broadband power Amplifier using AlGaN-GaN on Sic  
Huizhi Wang1,2*, Liang Li1, Yuxing Cui2, Jiayun Yin1, Zhihong Feng1, Hongjiang Wu2  
1Science and Technology on ASIC Lab, China  
2Heibei Semiconductor Research Institute, China  
*Email: ewhz@hotmail.com |
| PA04     | 0042  | Integration of information security chips based on System-in-Package  
Tong Ran, Guoqiang Bai  
Tsinghua University, China  
Email: rt09@mails.tsinghua.edu.cn |
| PA05     | 0044  | A New Asynchronous Delay-Insensitive Link based on a 1-of-4 LETS Code  
Can Wang, Qin Wang, Jianfei Jiang  
Shanghai Jiao Tong University, China  
Email: {wangcan, wangqin, jiangjianfei}@ic.sjtu.edu.cn |
| PA06     | 0051  | A High-speed Asynchronous Array Multiplier based on Multi-threshold Semi-Static NULL Convention Logic Pipeline  
Yanfei Yang1*, Student Member, IEEE, Yintang Yang1, Zhangming Zhu1, Duan Zhou2  
1 XiDian University, China  
2 Xidian University, China  
* Email: yfyang@xidian.edu.cn |
| PA07 | 0066 | An Ultra Low Power ASK Demodulator for Passive UHF RFID Tag  
Hongqiang Zong, Jinpeng Shen, Shan Liu, Mei Jiang, Qingyuan Ban, Ling Tang, Fanyu Meng, and Xin’an Wang*  
* wangxa@szpku.edu.cn |
| PA08 | 0072 | Improvement and Parallel Implementation of Canny Edge Detection Algorithm Based on GPU  
Shengxiao Niu, Jingjing Yang, Sheng Wang, Gengsheng Chen*  
* Email: gschen@fudan.edu.cn |
| PA09 | 0084 | A New Full Current Mode Sense Amplifier with Compensation Circuit  
Yiqi Wang, Fazhao Zhao, Mengxin Liu, Zhengsheng Han  
* Email: yqwang.wang@gmail.com |
| PA10 | 0092 | An Efficient 90nm Technology-Node GHz Transceiver of On-Chip Global Interconnect  
Zaixiao Zheng, Zhigang Mao, Jianfei Jiang  
* Email: zhengzaixiao@ic.sjtu.edu.cn |
| PA11 | 0109 | Electrochemical Biosensor Based on Modified Graphene Oxide for Tuberculosis Diagnosis  
Pei Zhang, Xiaosen Chai, Chun Xu, Jia Zhou*  
* Email: jia.zhou@fudan.edu.cn |
| PA12 | 0128 | Digital Quadrature IF modulator using single-bit DACs  
RuiminHuang,ChaodongLing,JiaxianWang  
* {huangruimin,eeda,wangjx}@hqu.edu.cn |
| PA13 | 0134 | Zero-Crossing Distortion Analysis in One Cycle Controlled Boost PFC for Low THD  
Yani LI 1*, Yintang YANG 1*, Zhangming ZHU 1*, Wei Qiang 2*  
1Xidian University, China  
2 Xi’an Longtium Microelectronics Technology Developing Co., Ltd, China |
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<th>Session</th>
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<tr>
<td>PA14</td>
<td>0150</td>
<td>A Simulation Study of Vertical Tunnel Field Effect Transistors</td>
<td>Zhong-Fang Han, Guo-Ping Ru*, Gang Ruan</td>
<td>Fudan University, China</td>
<td><a href="mailto:gpru@fudan.edu.cn">gpru@fudan.edu.cn</a></td>
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<td>PA15</td>
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<td>Determination of the trap states distribution in Poly-Si films using</td>
<td>Xiyue Li, Wanling Deng, Junkai Huang</td>
<td>Jinan University, China</td>
<td><a href="mailto:lixyue@tom.com">lixyue@tom.com</a></td>
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<td>the OEMS modulation</td>
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<td>PA16</td>
<td>0182</td>
<td>High efficiency and low power multi-rate LDPC decoder design for</td>
<td>Jiang xiaobo, li hongyuan</td>
<td>South China University of Technology,</td>
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<td>PA17</td>
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<td>Area efficient LDPC decoder design for parallel layered decoding</td>
<td>Yuan Yao, Fan Ye, and Junyan Ren</td>
<td>Fudan University, China</td>
<td><a href="mailto:fanye@fudan.edu.cn">fanye@fudan.edu.cn</a></td>
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<td>PA18</td>
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<td>A Decimation Filter's High-Efficiency Reuse on FPGA</td>
<td>Shengqiong Xie*, Huxiong Xu</td>
<td>Fudan University, China</td>
<td><a href="mailto:xsqShannon@gmail.com">xsqShannon@gmail.com</a></td>
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<td>PA19</td>
<td>0188</td>
<td>Accelerating the Data Shuffle Operations for FFT Algorithms on SIMD</td>
<td>Kai Zhang*, Shuming Chen, Sheng Liu, Yaohua Wang, Junhui Huang</td>
<td>National University of Defense</td>
<td><a href="mailto:zhknudt@gmail.com">zhknudt@gmail.com</a></td>
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<td>DSPs</td>
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<td>Technology, Changsha 410073, China</td>
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<td>PA20</td>
<td>0189</td>
<td>Automatic Compilation Flow for a Coarse-grained Reconfigurable</td>
<td>Hao Wang, Weiguang Sheng, Weifeng He</td>
<td>Shanghai Jiao Tong University, China</td>
<td><a href="mailto:haowang@ic.sjtu.edu.cn">haowang@ic.sjtu.edu.cn</a></td>
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<td>PA21</td>
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<td>Origin of High On-State Current For Dopant-Segregated Schottky MOSFET</td>
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<td>PA22</td>
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* Fudan University, China  
* Email: yljiang@fudan.edu.cn |
| PA23 | 0206 |
| **Libo Qian*, Zhangming Zhu, Yintang Yang**  
* Xidian University, China  
* Email: jerboaqian@hotmail.com |
| PA24 | 0223 |
| **Shan Cao*, Zhaolin Li, and Shaojun Wei**  
* Tsinghua University, China  
* Email: caos09@mails.tsinghua.edu.cn |
| PA25 | 0224 |
| **Wenjiang Liu1*, Yue Zhu, Tao Liu, Mengtian Rong, Hao Zhang**  
1 Shanghai Jiao Tong University, China  
* Email: liuwenjiang@sjtu.edu.cn |
| PA26 | 0237 |
| **Akitoshi Matsuda1,2*, Shinichi Baba2**  
1 Kyushu University, Japan  
2 Kyushu Embedded Forum, Japan  
* Email: matsuda_aki@slrc.kyushu-u.ac.jp |
| PA27 | 0238 |
| **Huidong Zhao*, Yong Hei, Shushan Qiao**  
* Institute of Microelectronics of Chinese Academy of Sciences, China  
* Email: hdzhao2003@126.com |
| PA28 | 0239 |
| **Daying Sun, Shen Xu, Weifeng Sun, Shengli Lu, Longxing Shi**  
* Southeast University, China  
E-mail: hasdysun@seu.edu.cn |
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| PA30 | 0273 | Research on Reconfigurable Multiplier Unit Based on GF[(28)]4 Field of Symmetric Cryptography  
Xu JianBo1 Dai Zibin1 Xuan Yang2 Su Yang1  
1 Zhengzhou Information Science and Technology Institute, China  
2 Jiangnan Institute of Computing Technology, China  
E-mail: yulonggongwu05@163.com |
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| PA31 | 0276 | A Low-Voltage Differential Injection Locked Divider with Forward Body Bias  
Haipeng Fu, Hanchao Zhou, Yangyang Niu, Junyan Ren*, Wei Li*, Ning Li  
Fudan University, Shanghai, China  
* Email: {jyren, w-li}@fudan.edu.cn |
| PA32 | 0280 | Effects of unintended dopants on I-V characteristics of the double-gate MOSFETs, a simulation study  
Peicheng Li, Guanghui Mei, Guangxi Hu*, Ran Liu, and Tingao Tang  
Fudan University, China  
* Email: gxhu@fudan.edu.cn |
| PA33 | 0282 | A Control Scheme for a 65nm 32x32b 4-read 2-write Register File  
Jun Han, Xingxing Zhang, Baoyu Xiong, Zhiyi Yu*, Xiaoyang Zeng  
Fudan University, China  
* Email: zhiyiyu@fudan.edu.cn |
| PA34 | 0291 | Reflection Analysis of Signal Transmission in 32-bit CPU Based SiP  
Zerong Tao, Liji Wu*, Xiangmin Zhang  
Tsinghua University, China  
* Email: lijiwu@mail.tsinghua.edu.cn |
| PA35 | 0293 | Research on Testing of 32-bit CPU Based SiP  
Chunlin Xie, Liji Wu*, Xiangmin Zhang  
Tsinghua University, China  
* Email: lijiwu@mail.tsinghua.edu.cn |
| PA36 | 0296 | A Confidential RFID Model to Prevent Unauthorized Access  
*Maryam Gharooni, Mazdak Zamani (Universiti Teknologi Malaysia) Mehdi Mansourizadeh(Multimedia University) Shahidan Abdullah(Universiti Teknologi Malaysia) |
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| **PA37** | 0342 | A Security Processor Based on MIPS 4KE Architecture  
Shuai Wang1, Yang Li, Junbao Liu, Jun Han*, and Xiaoyang Zeng  
*Email: junhan@fudan.edu.cn |
| **PA38** | 0345 | Design of A Reconfigurable Network Interface Processor  
Lei Zhang1*, Tao Li 2, Zhentao Li 1, Lin Jiang 1  
1 Xi’an University of Posts and Telecom, China  
2 Shaanxi Provincial Research Center of Telecom ASIC Design, China  
zanglei_felix@163.com |
| **PA39** | 0347 | A Hardware Accelerator for Speech Recognition Applications  
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*Email: chenyun@fudan.edu.cn |
| **PA40** | 0353 | A Programmable IP Core for LDPC Decoder Based on ASIP  
Jun Deng1*, Bing Li 2, Lintao Liu3, Rui Chen2  
1 Sichuan Institute of Solid-state Circuits, China  
2 Southeast University, Nanjing, China  
3 Science and Technology on Analog Integrated Circuit Laboratory, China  
*Email: dj_richard@126.com |
| **PA41** | 0356 | Parallel Structure of GF (2^14) and GF (2^16) Multipliers Based on Composite Finite Fields  
Jianing Su, Zhenghao Lu*  
Soochow University, Suzhou, Jiangsu, 215006, China  
*Email: luzhh@suda.edu.cn |
| **PA42** | 0365 | Novel CMOS Schmitt triggers using floating-gate MOS transistors  
Guoqiang Hang1,2*, Hongli Zhu1, Peiyi Zhao3, Xuanchang Zhou1  
1 Zhejiang University City College, China  
2 Zhejiang University, China  
3 Chapman University, USA  
*Email: hanggg@mail.hz.zj.cn |
| **PA43** | 0380 | A New Method to Improve the Unconditional Stability of InGaP/GaAs Heterojunction Bipolar Transistor  
Shanggong Feng1, Yanhu Chen1*, Huijun Li1, Minghua Zhang2.  
1 Shan Dong University, China  
2 Artillery Command Academy, China  
*Email: chenyanhui@sdu.edu.cn |
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<td>Jinran Du, Wanghui Zou, Xuecheng Zou*</td>
<td>Huazhong University of Science and Technology, China</td>
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<td>PA45</td>
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<td>Junjuan Liu, Xi Tan, Hao Min</td>
<td>Fudan University, China</td>
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<td>PA46</td>
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<td>Zhouyi Liu, Chiu-Sing Choy</td>
<td>The Chinese University of Hong Kong, Hong Kong</td>
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<td>Yucheng Liu, Jing Xie, Zhigang Mao</td>
<td>Shanghai Jiao Tong University, China</td>
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<td>PA48</td>
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<td>A permutation network for configurable and scalable FFT processors</td>
<td>Shuai Chen, Jialin Chen, Kanwen Wang, Wei Cao*, Lingli Wang, Member, IEEE</td>
<td>Fudan University, China</td>
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<td>PA49</td>
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<td>Pan Hao1,Hong Qi12*,Du Jiaqin1,Pan Pan1</td>
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<td>2 State Key Lab of ASIC, Fudan University, China</td>
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<td>PA50</td>
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<td>Simulation of Carrier Transport in Quantum Cascade Lasers</td>
<td>Yingying Li1, Guo-Ping Ru1*, Z.-M. Simon Li2</td>
<td>Fudan University, China</td>
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<td>2 Crosslight Software Inc., Canada</td>
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<td>PA51</td>
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<td>Design of a UHF RFID Tag Baseband with the Hummingbird Cryptographic Engine</td>
<td>Mengqin Xiao, Xiang Shen and Junyu Wang</td>
<td>Fudan University, China</td>
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<td>Joseph Crop</td>
<td>Oregon State University, USA</td>
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<td>A study on channel polarization and polar coding</td>
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<td>PB01</td>
<td>TSV Based 3D IC Wire Length Calculation Algorithm</td>
<td>Ligang Hou*, Shu Bai, Jinhui Wang White University of Technology, China * Email: <a href="mailto:houligang@bjut.edu.cn">houligang@bjut.edu.cn</a></td>
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<td>Xiao Zhao, Huajun Fang and Jun Xu Tsinghua University, China E-mail: <a href="mailto:hjfang@tsinghua.edu.cn">hjfang@tsinghua.edu.cn</a></td>
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<td>PB03</td>
<td>Ultra low voltage, wide tuning range voltage controlled ring oscillator</td>
<td>Li Tianwang1, Jiang Jinguang 1*, Ye Bo2, Han Xingcheng1,2 Wuhan University Wuhan, China 2 Shanghai University of Electric Power, China E-mail: <a href="mailto:jgjiang11@yahoo.com.cn">jgjiang11@yahoo.com.cn</a></td>
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<td>PB04</td>
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<td>PB05</td>
<td>A 2.5V Supply Low Noise CMOS Amplifier Using Noise Reduction Technique of Chopper Stabilization</td>
<td>Hossein yahyatabar1*, Prof.Farhad Razaghian1, Mehran Yahyavi1, Mohsen Habib Nezhad2 Islamic Azad University 2 Multi Media University * Email:<a href="mailto:h_y892@yahoo.com">h_y892@yahoo.com</a></td>
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<td>PB06</td>
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<td>PB07</td>
<td>Efficient Floating Random Walk Algorithm for Interconnect Capacitance Extraction Considering Multiple Dielectrics</td>
<td>Gang Hu1, Wenjian Yu1*, Hao Zhuang1,2, Shan Zeng3 Tsinghua University, China 2 Peking University, China 3 China University of Geosciences, China * Email:<a href="mailto:yu-wj@tsinghua.edu.cn">yu-wj@tsinghua.edu.cn</a></td>
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<td>PB08</td>
<td>A Dual Mode High efficiency Buck DC-DC Converter</td>
<td>Xu Gong, Jinguang Jiang*, Xifeng Zhou Wuhan University, China e-mail: <a href="mailto:jgjiang09@yahoo.com.cn">jgjiang09@yahoo.com.cn</a></td>
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| PB09  | 0076 | A WiMAX Receiver Front-End for 3.4GHz-3.7GHz Licensed Band Application  
Zhe-Yang Huang  
*Industrial Technology Research Institute (ITRI)*  
maverick43456@cycu.org.tw |
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| PB10  | 0091 | A Novel Transimpedance Amplifier for 10 Gbit/s Optical Communication System  
Taiyi Huang, Qihui Zhang, Weifeng Zhang*  
*Henan University, China*  
*Email: wfzhang@henu.edu.cn |
| PB11  | 0098 | Single Event Upset Mitigation for FDP2008  
Meng Yang1*, Gengsheng Chen1  
1 Fudan University, China  
*Email: mengyang@fudan.edu.cn |
| PB12  | 0099 | Electro-thermal Model Extraction of Power GaN HEMT Using I-V Pulsed and DC Measurements  
Zhifu Hu, Xuebang Gao, Shujun Cai  
*Hebei Semiconductor Research Institute, China*  
zhifuhu_2006@yahoo.com.cn |
| PB13  | 0103 | Characterization and Analysis of Pattern dependent Variation-aware Interconnects for a 65nm Technology  
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*Shanghai Research Institute of Microelectronics, Peking University, China*  
*Semiconductor Manufacturing International Corporation (SMIC), China* |
| PB14  | 0108 | Integrated Gm-C Based PI Controller for MEMS Gyroscope Drive  
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1 Chinese Academy of Sciences, China  
2 Graduate University of CAS, China  
3 Shanghai Institute of Microsystem and Information Technology, CAS, China  
*Email: yanghg@mail.ie.ac.cn |
| PB15  | 0116 | Subthreshold MOSFET Bandgap for Ultra-Low Supply Voltage  
Yilei Li*, Yu Wang, Na Yan, Xi Tan and Hao Min  
*Fudan University, China*  
*Email: liyilui@gmail.com |
| PB16  | 0123 | A Highly Linear Wideband Variable Gain CMOS Balun-LNA  
Hui Wang, Taotao Yan, Dongpo Chen, and Jianjun Zhou*, Senior Member, IEEE |
| PB17 | 0131 | Shanghai Jiao Tong University, China  
*Email: zhoujianjun@sjtu.edu.cn | A 65nm 10MHz Single-Inductor Dual-Output Switching Buck Converter with Time-Multiplexing Control  
Miao Yang, Weifeng Sun, Shen Xu, Shengli Lu, Longxing Shi  
Southeast University, China  
E-mail: myron@seu.edu.cn |
| PB18 | 0133 | Design and Implementation of pipelined TMVP multiplier using block recombination  
Xiao Ma1* and Guoqiang Bai1*  
1 Tsinghua University, China  
* Email: ma-x09@tsinghua.edu.cn  
baigq@mail.tsinghua.edu.cn |
| PB19 | 0135 | A Novel Low THD 4-Quadrant Analog Multiplier Using Feedforward Compensation for PFC  
Yani Li 1*, Yintang YANG 1*, Zhangming ZHU 1*, Wei Qiang 2*  
1 Xidian University, China  
2 Xi’an Longtium Microelectronics Technology Developing Co., Ltd, China  
* Email: yanili@mail.xidian.edu.cn |
| PB20 | 0137 | An Inductorless CMOS LNA with Single input and Differential output  
Jinguang Jiang, Qingyun Li, Xifeng Zhou  
Wuhan University, China  
Email: jgjiang09@yahoo.com.cn |
| PB21 | 0138 | A New Nonlinear Parameterized Model Order Reduction Technique Combining the Interpolation Method and Proper Orthogonal Decomposition  
Zhiyu Xu1,2, Xinnan Lin1,2*, Hao Zhuang1,2, Bo Jiang1,2, Haijun Lou1,2, Jin He2**  
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| PB22 | 0153 | A 0.6 ppm/°C Current-Mode Bandgap with Second-Order Temperature Compensation  
Yilei Li, Yu Wang, Na Yan*, Xi Tan and Hao Min  
Fudan University, China  
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| PB23 | 0156 | A 12-bit 50-MSPS SHA-less Opamp-Sharing Analog-to-Digital Converter in 65nm CMOS  
ChenShu1,Guanghua Shu1, Jun Xu1, Fan Ye1, Junyan Ren1*  
1Fudan University, China.  
*Email:jyren@fudan.edu.cn |
| PB24 | 0167 | A Low-Noise Amplifier Using Subthreshold Operation for |
| PB25  | 0168  | A sixth-order Chebyshev low-pass filter for single-chip UHF RFID Reader  
Tang Tang, Tingting Mo, and Dongpo Chen*  
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| PB26  | 0178  | A Realizable Reconstruction Filter for Sampled data Systems  
Muwahida Liaquat1*, Mohammad Bilal Malik1  
1 National University of Sciences and Technology, Pakistan  
* Email: muwahida@ee.ceme.edu.pk |
| PB27  | 0187  | Optimization of ADM with both restrictions of resolution and power dissipation in low supply voltage  
Shujuan Yin1*, Xiangyu Li2  
1 Beijing Information Science and Technology University, China  
2 Tsinghua University, China  
* Email: yinsj03@gmail.com |
| PB28  | 0190  | A Timing-Perspective Study on the Wire Model in Placement  
Liu Liu1, Yongqiang Lu2, Qiang Zhou1  
1 Tsinghua University, China  
2 Research Institute of Information Technology Tsinghua University, China  
E-mail: sammie0125@gmail.com |
| PB29  | 0193  | Design for testability of FFT/IFFT IP core for UWB systems  
Weilu Su1, Longzhao Shi2  
1 Fuzhou University, China  
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Email: worldsky1@gmail.com |
| PB30  | 0197  | A CMOS Hysteresis Undervoltage Lockout with Current Source Inverter Structure  
Chao Zhang, Zhijia Yang, Zhipeng Zhang  
Shenyang Institute of Automation Chinese Academy of Sciences, China  
Email: zhangchao@sia.cn |
| PB31  | 0201  | A high linearity MOS capacitor for low voltage applications  
Shujuan Yin1  
1 Beijing Information Science and Technology University, China  
Email: yinsj03@gmail.com |
| PB32  | 0205  | A Low-Kickback Preamplifier with Offset Cancellation For Pipelined Folding A/D Converter  
Xiaojuan Li*, Yintang Yang, Zhangming Zhu  
Xidian University, China |
| Page | 0213 | Auto-Assign Method for large scale flip-chip package design  
Haitao Han, Wen Yin, Wenqian Wang, Zegui Pang  
IBM GCG Systems & Technology Lab |
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| PB34 | 0230 | A 60GHz Power Amplifier using 90-nm RF-CMOS Technology  
Nan Zhang, Lingling Sun*, Senior Member, IEEE, Jincai Wen,  
Jun Liu, Jia Lou, Guodong Su, He Li  
Hangzhou Dianzi University, China  
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Xinrui Zhang, Liguang Chen, Liyun Wang, Jian Wang*, Jinmei Lai  
Fudan University, China  
*Email: jwang@fudan.edu.cn |
| PB36 | 0244 | Efficient Temporal Task Partition for Coarse-Grain Reconfigurable Systems  
Based on Simulated Annealing Genetic Algorithm  
Yifan Zhou, Weiguang Sheng*, Xie Liu, Weifeng He, Zhigang Mao  
Shanghai Jiaotong University, China  
*Email: wgshenghit@gmail.com |
| PB37 | 0246 | Improved Algorithm for Pareto Front Computation for CMOS Opamp Based on Multi-objective Genetic Optimization  
Peng Chen, Yushun Guo  
Hangzhou Dianzi University, China  
Email: michaeljoy0121@163.com |
| PB38 | 0253 | Calibration Method Considering Second-Order Error Term of Timing Skew for A Novel Multi-Channel ADC  
Yong-sheng Yin 1*, Rui Zhang, Jun Yang, Ming-lun Gao  
1 Hefei University of Technology, China  
* Email: yinyongsheng@hfut.edu.cn |
| PB39 | 0254 | 12 W/mm, 45% X-band AlGaN/GaN HEMTs on SiC  
Xinjiang Luo1*, Zhiguo Zhang2 Jingqiang Li2 jianbo Song2  
1 Hangzhou Dianzi University , China  
2 China Electronic Technology Group Corporation, China  
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| PB40 | 0256 | Large-Signal MOSFET Modeling by Means of Knowledge Based Fuzzy Logic System  
Liyuan Wang, Yushun Guo  
Hangzhou Dianzi University, China |
| PB41 | 0288 | Design and Application of Reusable SoC verification platform  
Lulu Feng, Zibin Dai, Wei Li, Jianlei Cheng  
Zhengzhou Information Science and Technology Institute, China  
Email: fenglulu05@126.com |
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Bo Li*, Liji Wu, Xiangmin Zhang  
* Tsinghua University, China  
* Email: lijiwu@tsinghua.edu.cn |
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Junwei He1, Liji Wu*, Xiangmin Zhang  
* Tsinghua University, China  
* Email: lijiwu@mail.tsinghua.edu.cn |
| PB44 | 0315 | A Method to Build Reconfigurable Architectures by Extracting Common Subgraphs  
Tianyun Zhang*, Rui Zhang*, Lingli Wang*, Yu Hu#  
* Fudan University, China  
# University of Alberta, Canada  
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# Email: bryanhu@ece.ualberta.ca |
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Biye Xu, Lenian He*  
Zhejiang University, China  
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Qin Wu, Wei Li, Ning Li, Junyan Ren  
Fudan University, China  
Email: w-li@fudan.edu.cn |
| PB47 | 0333 | Compensator Design for Digital Controlled Switched-Mode Power Supplies  
Ling Lin*, Jianping Qiu, Lenian He  
Zhejiang University, China  
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| PB48 | 0350 | (paper withdraw) |
| PB49 | 0355 | A 1.2-V 250-MS/s 8-bit Pipelined ADC in 0.13-μm CMOS  
Peiyuan Wan, Wei Lang, Di Fang, Wei Cui, and Pingfen Lin  
Beijing University of Technology, China  
wanpy@bjut.edu.cn |
| PB50 | 0358 | A Novel RSD Correction for Pipeline ADC  
Dawei Fu, Lenian He*, Biye Xu  
Zhejiang University, China  
Email: helenian@vlsi.zju.edu.cn |
| PB51 | 0389 | A Digital Sliding Mode controller for Switching Power Supply Converters  
Guannan Xu, Chen Jia, Chun Zhang, Zhihua Wang  
Tsinghua University, China  
Email: xgn09@mails.tsinghua.edu.cn, jiachen@mail.tsinghua.edu.cn |
| PB52 | 0391 | A Low-Power Gm-R-C Image Rejection Filter for Complex Low-IF Receiver  
Hao Li, Hong Zhang*, Xunwei Weng, Ruizhi Zhang  
Xi'an Jiaotong University, China  
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| PB53 | 0397 | A 4G5/s 3b Two-way Time-Interleaved ADC in 0.13um CMOS  
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Fudan University, China  
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| PB54 | 0429 | A Low Power 1.0 GHz VCO in 65nm-CMOS LP-Process  
Zhang Zhang1, Zhiyi Yu2*, Xu Cheng2, Xiaoyang Zeng2  
1 Hefei University of Technology, China  
2 Fudan University, China  
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| PB55 | 0440 | Three Stage Low Noise Operational Amplifier in 0.18 um CMOS  
Saber Izadpanah Tous*, Abbas Golmakani  
Sadjad University, IRAN  
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| PB56 | 0442 | A Charge-Pump Circuit to Restrain Reference Spurs in the PLL  
Changhong Huan1*, Xiushan Wu1, Dan Wang  
1 China Jiliang University, China  
2 Anhui University of Technology, China  
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| PB57 | 0361 | A Wide Lock-Range, Low Jitter Phase-Locked Loop for Multi-Standard SerDes Application  
Shaolong Liu *, Hui Wang, Yuhua Cheng (IEEE Fellow)  
Shanghai Research Institute of Microelectronics, Peking University, China  
* Email:shaolong.liu@pku.edu.cn |
| PB58 | 0030 | Ultralow-power Analog Front-End IC Design for an Implantable Cardioverter Defibrillator (ICD) (Invited)  
Weibo Hu1, Yen-Ting Liu1, Tam Nguyen1,2, Bosco Dsouza1 and Donald Y.C. Lie1,2,  
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